Appln. No. 10/783,873
Filing Date: February 20, 2004
Supp. Prel. Amdt. dated May 16, 2006

AMENDMENTS TO THE CLAIMS

Listing of Claims:

Claims 1-21 (Canceled).

Claim 22. (Previously Presented) A voice communication circuit comprising:

at least one processor capable of packetizing digitized voice information to produce at least one voice packet;

a buffer capable of storing the at least one voice packet;

interface circuitry capable of communicatively coupling the buffer with one of a plurality of interchangeable network interfaces, each of the plurality of interchangeable network interfaces supporting communication of voice packets via an associated type of communication network;

the at least one processor capable of determining the associated type of communication network supported by the one of the plurality of interchangeable network interfaces in communication with the interface circuitry; and

the at least one processor capable of arranging the transmission of voice packets via the one of the plurality of interchangeable network interfaces based upon the associated type of communication network.

Claim 23. (Previously Presented) The voice communication circuit of claim 22 wherein the voice packets are packetized according to an Internet protocol (IP).

Claim 24. (Previously Presented) The voice communication circuit of claim 23 wherein the Internet protocol (IP) comprises the transmission control protocol (TCP)/Internet protocol (IP).

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Claim 25. (Previously Presented) The voice communication circuit of claim 22 wherein the associated type of communication network is a wireless packet network.

Claim 26. (Previously Presented) The voice communication circuit of claim 25 wherein the wireless packet network communicates at approximately 2.4 gigahertz.

Claim 27. (Previously Presented) The voice communication circuit of claim 25 wherein the wireless packet network communicates using a frequency hopping spread spectrum technique.

Claim 28. (Previously Presented) The voice communication circuit of claim 25 wherein the wireless packet network communicates using a direct sequence spread spectrum technique.

Claim 29. (Previously Presented) The voice communication circuit of claim 22 wherein the associated type of communication network comprises a wired network.

Claim 30. (Previously Presented) The voice communication circuit of claim 29 wherein the wired network comprises an Ethernet compatible network.

Claim 31. (Previously Presented) The voice communication circuit of claim 29 wherein the wired network comprises a conventional telephone switching network.

Claim 32. (Previously Presented) The voice communication circuit of claim 22 wherein the interface circuitry is compatible with a Personal Computer Memory Card Interface Association (PCMCIA) standard.

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Claim 33. (Previously Presented) The voice communication circuit of claim 22 further comprising:

the at least one processor capable of reducing the quantity of voice packets communicated via a communication network by changing the packetization based upon a level of voice activity.

Claim 34. (Previously Presented) The voice communication circuit of claim 22 further comprising

converter circuitry capable of converting a voice stream into digitized voice information.

Claim 35. (Previously Presented) The voice communication circuit of claim 22 wherein the voice stream comprises an analog signal.

Claim 36. (Previously Presented) A voice communication circuit comprising:

interface circuitry capable of communicatively coupling with one of a plurality of interchangeable network interfaces, each of the plurality of interchangeable network interfaces capable of communicating via an associated type of communication network to receive at least one voice packet;

a buffer capable of storing the at least one voice packet;

at least one processor capable of depacketizing the at least one voice packet to produce digitized voice information;

the at least one processor capable of determining the associated type of communication network supported by the one of the plurality of interchangeable network interfaces in communication with the interface circuitry; and

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the at least one processor capable of arranging the reception of the at least one packet via the one of the plurality of interchangeable network interfaces based upon the associated type of communication network.

Claim 37. (Previously Presented) The voice communication circuit of claim 36 wherein the voice packets are packetized according to an Internet protocol (IP).

Claim 38. (Previously Presented) The voice communication circuit of claim 36 wherein the associated type of communication network is a wireless packet network.

Claim 39. (Previously Presented) The voice communication circuit of claim 38 wherein the wireless packet network communicates at approximately 2.4 gigahertz.

Claim 40. (Previously Presented) The voice communication circuit of claim 38 wherein the wireless packet network communicates using a frequency hopping spread spectrum technique.

Claim 41. (Previously Presented) The voice communication circuit of claim 38 wherein the wireless packet network communicates using a direct sequence spread spectrum technique.

Claim 42. (Previously Presented) The voice communication circuit of claim 36 wherein the associated type of communication network is a wired network.

Claim 43. (Previously Presented) The voice communication circuit of claim 42 wherein the wired network comprises an Ethernet compatible network.

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Claim 44. (Previously Presented) The voice communication circuit of claim 42 wherein the wired network comprises a conventional telephone switching network.

Claim 45. (Previously Presented) The voice communication circuit of claim 36 wherein the interface circuitry is compatible with a Personal Computer Memory Card Interface Association (PCMCIA) standard.

Claim 46. (Previously Presented) The voice communication circuit of claim 36 further comprising:

converter circuitry capable of converting the digitized voice information to produce a voice stream.

Claim 47. (Previously Presented) The voice communication circuit of claim 46 further comprising:

the at least one processor capable of adjusting the buffering of voice packets in order to minimize gaps in the voice stream.

Claim 48. (Previously Presented) The voice communication circuit of claim 47 wherein the adjusting is based upon a propagation delay of a communication network.

Claim 49. (Previously Presented) The voice communication circuit of claim 46 wherein the voice stream is an analog signal.

Claim 50. (Previously Presented) A machine-readable storage having stored thereon a computer program having a plurality of code sections for implementing a voice communication system, the voice communication system capable of accepting at any point in time one of a plurality of interchangeable network interfaces, each of the interchangeable network interfaces

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for use with an associated type of communication network, the code sections executable by a machine for causing the machine to perform the operations comprising:

detecting the presence of an interchangeable network interface;

determining the associated type of communication network for use with the detected interchangeable network interface;

establishing a packet voice call via the associated type of communication network;

converting analog voice information to transmit voice packets;

sending the transmit voice packets via the associated type of communication network using the interchangeable network interface;

receiving voice packets via the associated type of communication network using the interchangeable network interface; and

converting the received voice packets to analog voice information.

Claim 51. The machine-readable storage of claim 50 wherein the associated type of communication network comprises a wireless network.

Claim 52. The machine-readable storage of claim 51 wherein the wireless network communicates at approximately 2.4 gigahertz.

Claim 53. The machine-readable storage of claim 51 wherein the wireless network communicates using a frequency hopping spread spectrum technique.

Claim 54. The machine-readable storage of claim 50 wherein the associated type of communication network is a local area network.

Claim 55. The machine-readable storage of claim 50 wherein the associated type of communication network comprises an Ethernet compatible network.

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Claim 56. The machine-readable storage of claim 50 wherein the associated type of communication network comprises a conventional telephone switching network.

Claim 57. (Previously Presented) The machine-readable storage of claim 50 wherein converting analog voice information to transmit voice packets comprises:

reducing the volume of transmit voice packets based upon a level of voice activity.

Claim 58. (Previously Presented) The machine-readable storage of claim 50 wherein converting the received voice packets to analog voice information comprises:

buffering voice packets for an adjustable period of time to avoid gaps in the analog voice information.

Claim 59. (Previously Presented) The machine-readable storage of claim 58 wherein the adjustable period of time is based upon a propagation delay.

Claim 60. (Previously Presented) The machine readable storage of claim 50 further comprising:

adapting call setup of the voice communication system based upon the type of communication network.

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